- By combining the equation for power, $p = vi$, with Ohm's law, we can determine the power absorbed by a resistor:

$$p = i^2R = v^2/R.$$

(See page 32.)

- Circuits are described by nodes and closed paths. A **node** is a point where two or more circuit elements join. When just two elements connect to form a node, they are said to be **in series**. A **closed path** is a loop traced through connecting elements, starting and ending at the same node and encountering intermediate nodes only once each. (See pages 37–39.)

- The voltages and currents of interconnected circuit elements obey Kirchhoff's laws:
  - **Kirchhoff's current law** states that the algebraic sum of all the currents at any node in a circuit equals zero. (See page 37.)
  - **Kirchhoff's voltage law** states that the algebraic sum of all the voltages around any closed path in a circuit equals zero. (See page 38.)

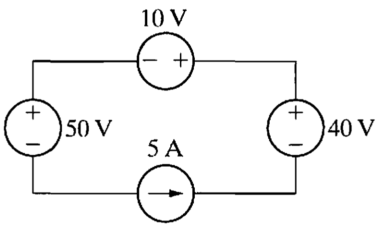
- A circuit is solved when the voltage across and the current in every element have been determined. By combining an understanding of independent and dependent sources, Ohm's law, and Kirchhoff's laws, we can solve many simple circuits.

Problems

Section 2.1

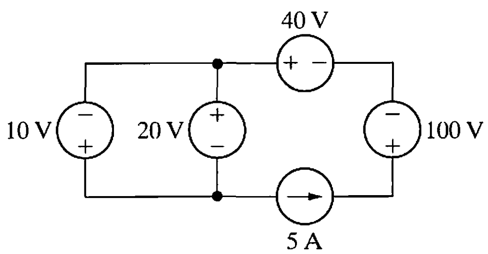
2.1 If the interconnection in Fig. P2.1 is valid, find the total power developed in the circuit. If the interconnection is not valid, explain why.

Figure P2.1



2.2 If the interconnection in Fig. P2.2 is valid, find the total power developed by the voltage sources. If the interconnection is not valid, explain why.

Figure P2.2



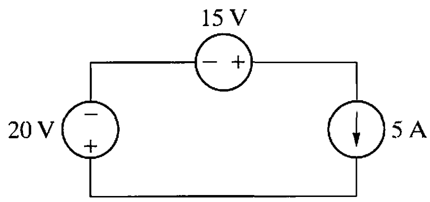
2.3 a) Is the interconnection of ideal sources in the circuit in Fig. P2.3 valid? Explain.

b) Identify which sources are developing power and which sources are absorbing power.

c) Verify that the total power developed in the circuit equals the total power absorbed.

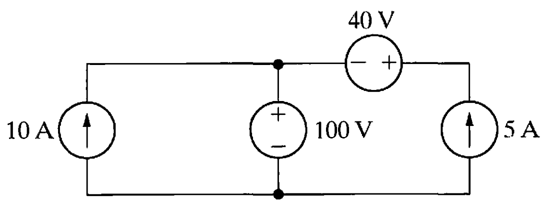
d) Repeat (a)–(c), reversing the polarity of the 20 V source.

Figure P2.3



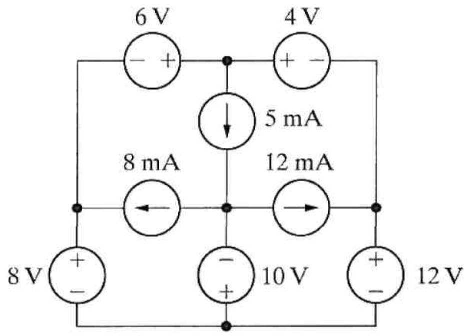
2.4 If the interconnection in Fig. P2.4 is valid, find the power developed by the current sources. If the interconnection is not valid, explain why.

Figure P2.4



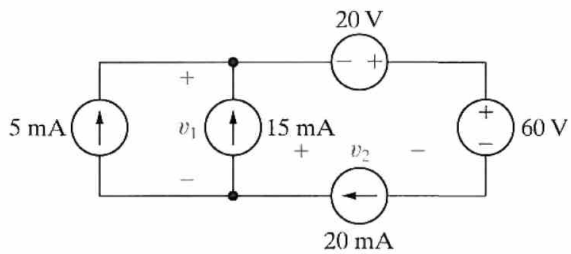
2.5 If the interconnection in Fig. P2.5 is valid, find the total power developed in the circuit. If the interconnection is not valid, explain why.

Figure P2.5



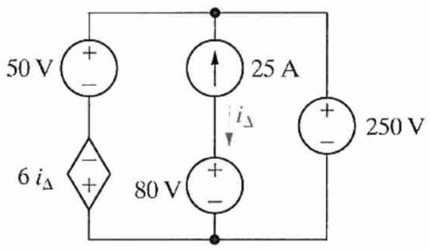
2.6 The interconnection of ideal sources can lead to an indeterminate solution. With this thought in mind, explain why the solutions for v_1 and v_2 in the circuit in Fig. P2.6 are not unique.

Figure P2.6



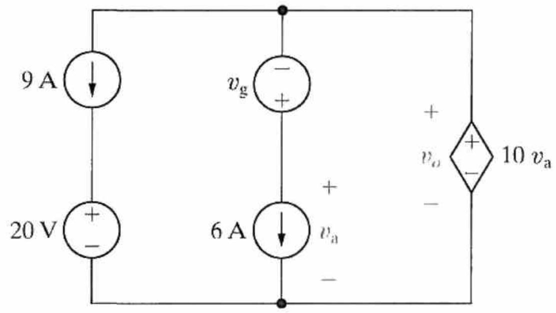
2.7 If the interconnection in Fig. P2.7 is valid, find the total power developed in the circuit. If the interconnection is not valid, explain why.

Figure P2.7



2.8 Find the total power developed in the circuit in Fig. P2.8 if $v_o = 5$ V.

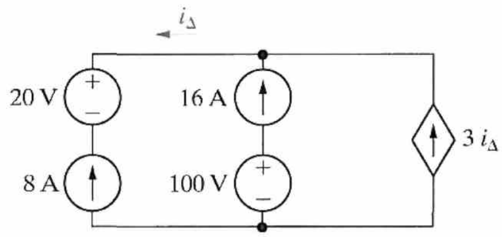
Figure P2.8



2.9 a) Is the interconnection in Fig. P2.9 valid? Explain.

b) Can you find the total energy developed in the circuit? Explain.

Figure P2.9



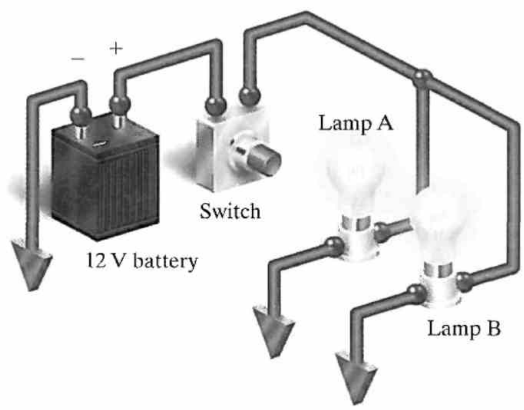
Sections 2.2–2.3

2.10 A pair of automotive headlamps is connected to a 12 V battery via the arrangement shown in Fig. P2.10. In the figure, the triangular symbol ▼ is used to indicate that the terminal is connected directly to the metal frame of the car.

a) Construct a circuit model using resistors and an independent voltage source.

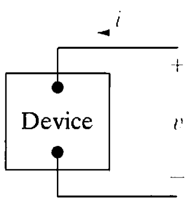
b) Identify the correspondence between the ideal circuit element and the symbol component that it represents.

Figure P2.10



2.11 The terminal voltage and terminal current were measured on the device shown in Fig. P2.11(a). The values of v and i are given in the table of Fig. P2.11(b). Use the values in the table to construct a circuit model for the device consisting of a single resistor from Appendix H.

Figure P2.11



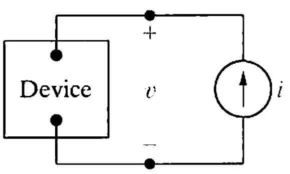
(a)

i (mA)	v (V)
−4	−108
−2	−54
2	54
4	108
6	162

(b)

2.12 A variety of current source values were applied to the device shown in Fig. P2.12(a). The power absorbed by the device for each value of current is recorded in the table given in Fig. P2.12(b). Use the values in the table to construct a circuit model for the device consisting of a single resistor from Appendix H.

Figure P2.12



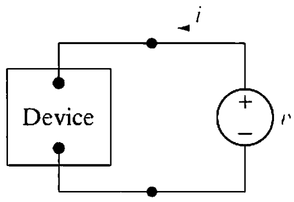
(a)

i (μA)	p (mW)
50	5.5
100	22.0
150	49.5
200	88.0
250	137.5
300	198.0

(b)

2.13 A variety of voltage source values were applied to the device shown in Fig. P2.13(a). The power absorbed by the device for each value of voltage is recorded in the table given in Fig. P2.13(b). Use the values in the table to construct a circuit model for the device consisting of a single resistor from Appendix H.

Figure P2.13



(a)

v (V)	p (mW)
−10	17.86
−5	4.46
5	4.46
10	17.86
15	40.18
20	71.43

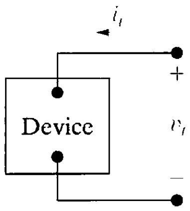
(b)

2.14 The voltage and current were measured at the terminals of the device shown in Fig. P2.14(a). The results are tabulated in Fig. P2.14(b).

a) Construct a circuit model for this device using an ideal current source and a resistor.

b) Use the model to predict the amount of power the device will deliver to a 20 Ω resistor.

Figure P2.14



(a)

v_t (V)	i_t (A)
100	0
120	4
140	8
160	12
180	16

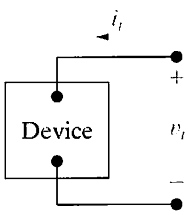
(b)

2.15 The voltage and current were measured at the terminals of the device shown in Fig. P2.15(a). The results are tabulated in Fig. P2.15(b).

a) Construct a circuit model for this device using an ideal voltage source and a resistor.

b) Use the model to predict the value of i_t when v_t is zero.

Figure P2.15



(a)

v_t (V)	i_t (A)
50	0
66	2
82	4
98	6
114	8
130	10

(b)

2.16 The table in Fig. P2.16(a) gives the relationship between the terminal current and voltage of the practical constant current source shown in Fig. P2.16(b).

a) Plot i_s versus v_s.

b) Construct a circuit model of this current source that is valid for $0 \le v_s \le 75$ V, based on the equation of the line plotted in (a).

c) Use your circuit model to predict the current delivered to a 2.5 kΩ resistor.

d) Use your circuit model to predict the open-circuit voltage of the current source.

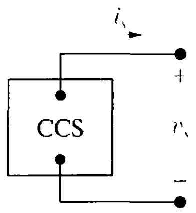
e) What is the actual open-circuit voltage?

f) Explain why the answers to (d) and (e) are not the same.

Figure P2.16

i_s (mA)	v_s (V)
20.0	0
17.5	25
15.0	50
12.5	75
9.0	100
4.0	125
0.0	140

(a)



(b)

2.17 The table in Fig. P2.17(a) gives the relationship between the terminal voltage and current of the practical constant voltage source shown in Fig. P2.17(b).

a) Plot v_s versus i_s.

b) Construct a circuit model of the practical source that is valid for $0 \le i_s \le 24$ mA, based on the equation of the line plotted in (a). (Use an ideal voltage source in series with an ideal resistor.)

c) Use your circuit model to predict the current delivered to a 1 kΩ resistor connected to the terminals of the practical source.

d) Use your circuit model to predict the current delivered to a short circuit connected to the terminals of the practical source.

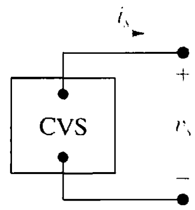
e) What is the actual short-circuit current?

f) Explain why the answers to (d) and (e) are not the same.

Figure P2.17

v_s (V)	i_s (mA)
24	0
22	8
20	16
18	24
15	32
10	40
0	48

(a)



(b)

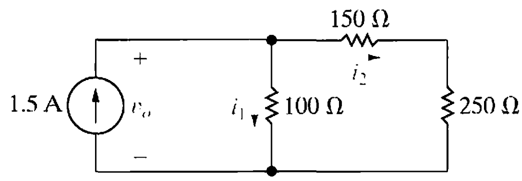
Section 2.4

2.18 PSPICE MULTISIM a) Find the currents i_1 and i_2 in the circuit in Fig. P2.18.

b) Find the voltage v_o.

c) Verify that the total power developed equals the total power dissipated.

Figure P2.18



2.19 PSPICE MULTISIM Given the circuit shown in Fig. P2.19, find

a) the value of i_a,

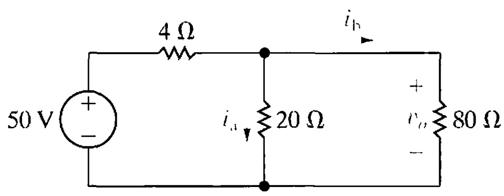
b) the value of i_b,

c) the value of v_o,

d) the power dissipated in each resistor,

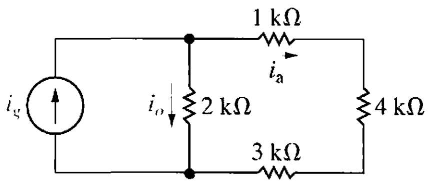
e) the power delivered by the 50 V source.

Figure P2.19



2.20 PSPICE MULTISIM The current i_a in the circuit shown in Fig. P2.20 is 2 mA. Find (a) i_o; (b) i_g; and (c) the power delivered by the independent current source.

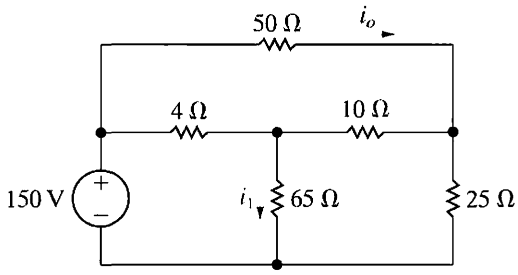
Figure P2.20



2.21 The current i_o in the circuit in Fig. P2.21 is 1 A.
PSPICE MULTISIM

a) Find i_1.
b) Find the power dissipated in each resistor.
c) Verify that the total power dissipated in the circuit equals the power developed by the 150 V source.

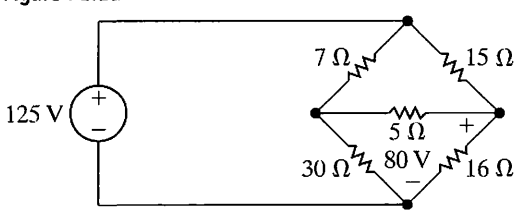
Figure P2.21



2.22 The voltage across the 16 Ω resistor in the circuit in Fig. P2.22 is 80 V, positive at the upper terminal.
PSPICE MULTISIM

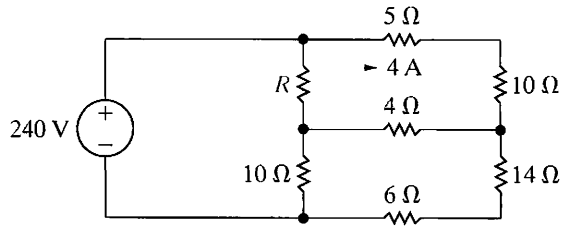
a) Find the power dissipated in each resistor.
b) Find the power supplied by the 125 V ideal voltage source.
c) Verify that the power supplied equals the total power dissipated.

Figure P2.22



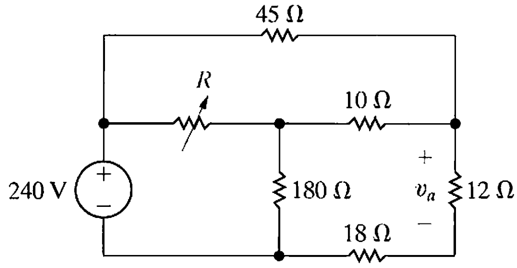
2.23 For the circuit shown in Fig. P2.23, find (a) R and (b) the power supplied by the 240 V source.
PSPICE MULTISIM

Figure P2.23



2.24 The variable resistor R in the circuit in Fig. P2.24 is adjusted until v_a equals 60 V. Find the value of R.
PSPICE MULTISIM

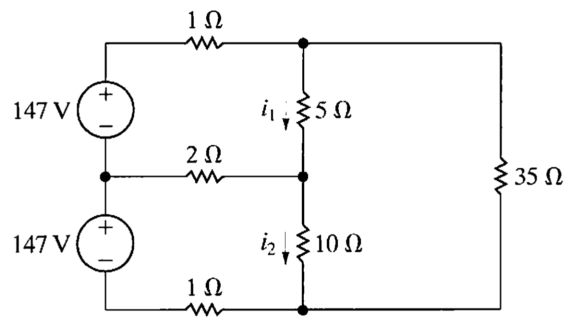
Figure P2.24



2.25 The currents i_1 and i_2 in the circuit in Fig. P2.25 are 21 A and 14 A, respectively.

a) Find the power supplied by each voltage source.
b) Show that the total power supplied equals the total power dissipated in the resistors.

Figure P2.25



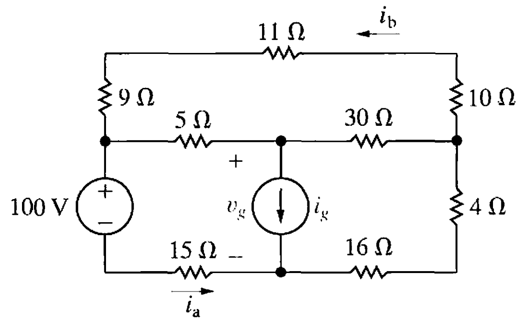
2.26 The currents i_a and i_b in the circuit in Fig. P2.26 are 4 A and −2 A, respectively.
PSPICE MULTISIM

a) Find i_g.
b) Find the power dissipated in each resistor.

c) Find v_g.

d) Show that the power delivered by the current source is equal to the power absorbed by all the other elements.

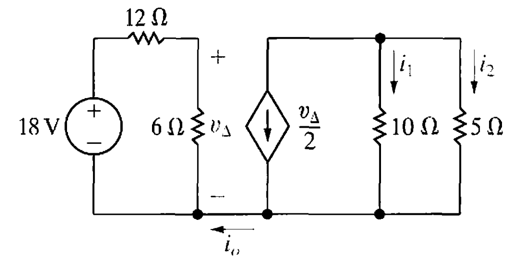
Figure P2.26



Section 2.5

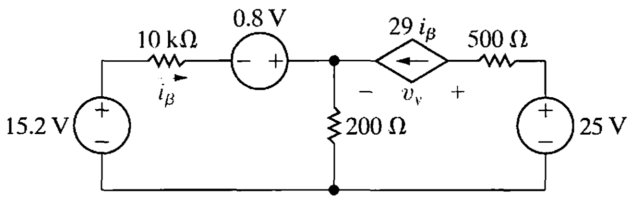
2.27 Find (a) i_o, (b) i_1, and (c) i_2 in the circuit in Fig. P2.27.
PSPICE
MULTISIM

Figure P2.27



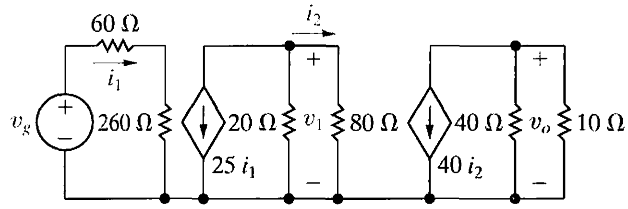
2.28 a) Find the voltage v_y in the circuit in Fig. P2.28.
PSPICE
MULTISIM
b) Show that the total power generated in the circuit equals the total power absorbed.

Figure P2.28



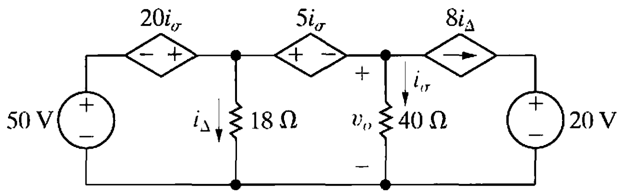
2.29 Find v_1 and v_g in the circuit shown in Fig. P2.29 when v_o equals 5 V. (*Hint:* Start at the right end of the circuit and work back toward v_g.)
PSPICE
MULTISIM

Figure P2.29



2.30 For the circuit shown in Fig. P2.30, calculate (a) i_Δ and v_o and (b) show that the power developed equals the power absorbed.
PSPICE
MULTISIM

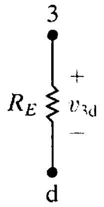
Figure P2.30



2.31 Derive Eq. 2.25. *Hint:* Use Eqs. (3) and (4) from Example 2.11 to express i_E as a function of i_B. Solve Eq. (2) for i_2 and substitute the result into both Eqs. (5) and (6). Solve the "new" Eq. (6) for i_1 and substitute this result into the "new" Eq. (5). Replace i_E in the "new" Eq. (5) and solve for i_B. Note that because i_{CC} appears only in Eq. (1), the solution for i_B involves the manipulation of only five equations.

2.32 For the circuit shown in Fig. 2.24, $R_1 = 40\,\text{k}\Omega$, $R_2 = 60\,\text{k}\Omega$, $R_C = 750\,\Omega$, $R_E = 120\,\Omega$, $V_{CC} = 10$ V, $V_0 = 600$ mV, and $\beta = 49$. Calculate i_B, i_C, i_E, v_{3d}, v_{bd}, i_2, i_1, v_{ab}, i_{CC}, and v_{13}. (*Note:* In the double subscript notation on voltage variables, the first subscript is positive with respect to the second subscript. See Fig. P2.32.)
PSPICE
MULTISIM

Figure P2.32



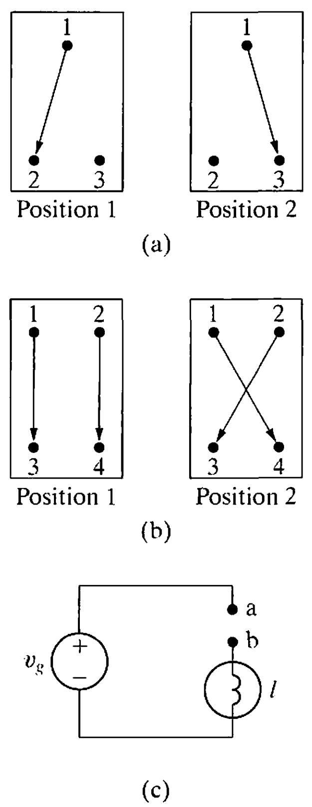
Sections 2.1–2.5

2.33 DESIGN PROBLEM It is often desirable in designing an electric wiring system to be able to control a single appliance from two or more locations, for example, to control a lighting fixture from both the top and bottom of a stairwell. In home wiring systems, this type of control is implemented with three-way and four-way switches. A three-way switch is a three-terminal, two-position switch, and a four-way switch is a four-terminal, two-position switch. The switches are shown schematically in Fig. P2.33(a), which illustrates a three-way switch, and P2.33(b), which illustrates a four-way switch.

a) Show how two three-way switches can be connected between a and b in the circuit in Fig. P2.33(c) so that the lamp l can be turned ON or OFF from two locations.

b) If the lamp (appliance) is to be controlled from more than two locations, four-way switches are used in conjunction with two three-way switches. One four-way switch is required for each location in excess of two. Show how one four-way switch plus two three-way switches can be connected between a and b in Fig. P2.33(c) to control the lamp from three locations. (*Hint:* The four-way switch is placed between the three-way switches.)

Figure P2.33



2.34 PRACTICAL PERSPECTIVE a) Suppose the power company installs some equipment that could provide a 250 V shock to a human being. Is the current that results dangerous enough to warrant posting a warning sign and taking other precautions to prevent such a shock? Assume that if the source is 250 V, the resistance of the arm is 400 Ω, the resistance of the trunk is 50 Ω, and the resistance of the leg is 200 Ω. Use the model given in Fig. 2.25(b).

b) Find resistor values from Appendix H that could be used to build a circuit whose behavior is the closest to the model described in part (a).

2.35 PRACTICAL PERSPECTIVE Based on the model and circuit shown in Fig. 2.25, draw a circuit model of the path of current through the human body for a person touching a voltage source with both hands who has both feet at the same potential as the negative terminal of the voltage source.

2.36 PRACTICAL PERSPECTIVE a) Using the values of resistance for arm, leg, and trunk provided in Problem 2.34, calculate the power dissipated in the arm, leg, and trunk.

b) The specific heat of water is 4.18×10^3 J/kg°C, so a mass of water M (in kilograms) heated by a power P (in watts) undergoes a rise in temperature at a rate given by

$$\frac{dT}{dt} = \frac{2.39 \times 10^{-4} P}{M} \text{ °C/s.}$$

Assuming that the mass of an arm is 4 kg, the mass of a leg is 10 kg, and the mass of a trunk is 25 kg, and that the human body is mostly water, how many seconds does it take the arm, leg, and trunk to rise the 5°C that endangers living tissue?

c) How do the values you computed in (b) compare with the few minutes it takes for oxygen starvation to injure the brain?

2.37 PRACTICAL PERSPECTIVE A person accidently grabs conductors connected to each end of a dc voltage source, one in each hand.

a) Using the resistance values for the human body provided in Problem 2.34, what is the minimum source voltage that can produce electrical shock sufficient to cause paralysis, preventing the person from letting go of the conductors?

b) Is there a significant risk of this type of accident occurring while servicing a personal computer, which typically has 5 V and 12 V sources?