

PGENE503 - Sistemas de Tempo-Real

Sugestões de Seminário

- 1) Real-time UML (UML Profile for Schedulability, Performance, and Time Specification)
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- 9) Real-time CORBA, OMG, 1999
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- 12) Lin. Issues on real-time systems programming: language, compiler and object orientation.
- 13) Buttazzo e Stankovic. RED: Robust Earliest Deadline Scheduling.
- 14) A. Burns and A.J. Wellings. HRT-HOOD: A Structured Design Method for Hard Real-time Systems.
- 15) Partha S. Roop A. Sowmya. Hidden Time Model for Specification and Verification of Embedded Systems.
- 16) Ren e Agha. RTsynchronizer: language support for real-time specifications in distributed systems.
- 17) Palopoli, Buttazzo, e Ancilotti. A C language extension for programming real-time applications.

- 18) Bertossi e Fusiello. Fault-Tolerant Deadline-Monotonic Algorithm for Scheduling Hard-Real-Time Tasks.
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- 21) Richard A. Kemmerer e Paul Z. Kolano. *Formally Specifying and Verifying Real-Time Systems*.
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- 23) Uckun e Gasperoni. Making Java real-time. *IEEE Spectrum*. Dez 1998.
- 24) Jakob Axelsson. Hardware/Software partitioning aiming at fulfillment of real-time constraints.
- 25) Ozgur Celebican, Tajana Simunic Rosing, and Vincent J. Money. Energy Estimation of Peripheral Devices in Embedded Systems.
- 26) Cecilia Ekelin and Jan Jonsson. Real-Time System Constraints: Where do They Come From and Where do They Go?
- 27) H. Chetto e M. Chetto. Some Results of the Earliest Deadline Scheduling Algorithm. *IEEE Trans. Soft. Engineering* 15(10), 1989.
- 28) Xu e Parnas. Scheduling process with release times, deadlines, precedence, and exclusion relations. *IEEE Trans. Soft. Eng.* 16(3), 1990.
- 29) Alessandro Cimatti, Andrea Micheli, Iman Narasamdya, and Marco Roveri. Verifying SystemC: a software model checking approach. In *FMCAD*, 2010.
- 30) Sudipta Chattopadhyay and Abhik Roychoudhury. Scalable and Precise Refinement of Cache Timing Analysis via Model Checking. The National University of Singapore.
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- 34) Cohen, D., BARRETO, R. S., Oliveira, H., Cordeiro, L. C. A car racing based strategy for the Dynamic Voltage and Frequency Scaling technique. In *21st International Symposium on Industrial Electronics (ISIE)*, pp. 774-779, IEEE, 2012.
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